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L49	116 and (((simultaneous\$2 or concurrent\$2) with ((read\$3 or load\$3) and (stor\$3 or writ\$3)))) same ((ram or sdram or dram or sram) and (non-volatile or nonvolatile or flash or eeprom)))	14	L4
L48	116 and (((simultaneous\$2 or concurrent\$2) with ((read\$3 or load\$3) and (stor\$3 or writ\$3)))) same ((volatile or ram or sdram or dram or sram) and (non-volatile or nonvolatile or flash or eeprom)))	32	L4
L47	116 and ((smartcard or smart-card or (single adj1 package) or (smart adj card)) same ((volatile or ram or sdram or dram or sram) and (non-volatile or nonvolatile or flash or eeprom)))	24	L4
L46	116 and (smartcard or smart-card or (single adj1 package) or (smart adj card))	86	L4
L45	116 and (smartcard or smart-card or (single adj1 package))	40	L4
L44	L42 and (ram)	4	L4
L43	L42 and (ram with table)	3	L4
L42	6684289.pn. or 20040123020.pn. or 20040111555.pn. or 20040064636.pn.	4	L4
L41	139 and ((volatile or ram or sdram or dram or sram) and (non-volatile or nonvolatile or flash or eeprom))	18	L4
L40	139 and pseudo\$	3	L4

<u>L39</u>	l16 and (page with (spare and data))	24	<u>L3</u>
<u>L38</u>	l36 and L37	41	<u>L3</u>
<u>L37</u>	L16 and ((non-volatile or nonvolatile or flash or eeprom) same((partition\$3 or area or location) with (data and (backup or redundant or parity or ecc))))	95	<u>L3</u>
<u>L36</u>	L16 and ((volatile or ram or sdram or dram or sram) same((partition\$3 or area or location) with (data and (backup or redundant or parity or ecc))))	69	<u>L3</u>
<u>L35</u>	L34 and l33	87	<u>L3</u>
<u>L34</u>	L16 and ((volatile or ram or sdram or dram or sram) same((partition\$3 or area or location) same (data and (backup or redundant or parity or ecc))))	143	<u>L3</u>
<u>L33</u>	L16 and ((non-volatile or nonvolatile or flash or eeprom) same((partition\$3 or area or location) same (data and (backup or redundant or parity or ecc))))	166	<u>L3</u>
<u>L32</u>	L16 and ((non-volatile or nonvolatile or flash or eeprom) and ((partition\$3 or area or location) same (data and (backup or redundant or parity or ecc))))	379	<u>L3</u>
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<u>L29</u>	L16 and ((non-volatile or nonvolatile or flash or eeprom) with ((partition\$3 or area or location) with(data and redundant)))	25	<u>L2</u>
<u>L28</u>	L16 and ((volatile or ram) with ((partition\$3 or area or location) with(data and redundant)))	8	<u>L2</u>
<u>L27</u>	L16 and ((volatile or ram) with ((partition\$3 or area or location) near3(data and redundant)))	2	<u>L2</u>
<u>L26</u>	L16 and ((volatile or ram or sdram or dram or sram) with ((partition\$3 or area or location) with ((data or bit\$1) near3 (ecc or parity or control or management)))) and ((non-volatile or nonvolatile or flash or eeprom) with ((partition\$3 or area or location) with ((data or bit\$1) near3 (ecc or parity or control or management))))	36	<u>L2</u>
<u>L25</u>	L16 and ((volatile or ram or sdram or dram or sram) with ((partition\$3 or area or location) with (ecc or parity or control or management))) and ((non-volatile or nonvolatile or flash or eeprom) with ((partition\$3 or area or location) with (ecc or parity or control or management)))	131	<u>L2</u>
<u>L24</u>	L23 and l22	5	<u>L2</u>
<u>L23</u>	L16 and ((non-volatile or nonvolatile or flash or eeprom) with ((partition\$3 or area or location) with (ecc or parity)))	22	<u>L2</u>
<u>L22</u>	L16 and ((volatile or ram or sdram or dram or sram) with ((partition\$3 or area or location) with (ecc or parity)))	15	<u>L2</u>
<u>L21</u>	L16 and ((volatile or ram) with ((partition\$3 or area or location) with (ecc or parity)))	5	<u>L2</u>
<u>L20</u>	L16 and ((volatile or ram) with ((partition\$3 or area or location) near3(ecc or parity)))	1	<u>L2</u>
<u>L19</u>	L16 and ((volatile or ram) with (ecc or parity))	83	<u>L1</u>
<u>L18</u>	L16 and ((volatile or ram) with ((ecc or parity) near2 bit\$1))	11	<u>L1</u>
<u>L17</u>	L16 and ((volatile or ram) with spare with actual)	2	<u>L1</u>
<u>L16</u>	l14 or L15	6184	<u>L1</u>
<u>L15</u>	711/170,173.ccls.	1785	<u>L1</u>
<u>L14</u>	l7 or 711/168.ccls.	4834	<u>L1</u>
<u>L13</u>	L9 and ((ram or volatile) with actual with spare)	1	<u>L1</u>
<u>L12</u>	L9 and ((ram or volatile) same actual same spare)	1	<u>L1</u>
<u>L11</u>	L9 and (actual same spare)	1	<u>L1</u>
<u>L10</u>	L9 and before	1	<u>L1</u>
<u>L9</u>	20020120820.pn.	1	<u>L9</u>
<u>L8</u>	20020120820.pn.L7	4553	<u>L8</u>
<u>L7</u>	l1 or 711/167,169.ccls.	4553	<u>L7</u>

<u>L6</u>	l4 and ((volatile or ram or sdram or dram or sram) and (non-volatile or nonvolatile or flash or eeprom))	2106	<u>L6</u>
<u>L5</u>	L4 and (pseudo-access\$3 or (pseudo near2 access\$3))	6	<u>L5</u>
<u>L4</u>	l1 or l2 or L3	4453	<u>L4</u>
<u>L3</u>	714/52,702.ccls.	298	<u>L3</u>
<u>L2</u>	365/185.11.ccls.	918	<u>L2</u>
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1 A novel NVRAM cell technology for high density applications

Yasmauchi, Y.; Tanaka, K.; Sakiyama, K.;

Electron Devices Meeting, 1988. Technical Digest., International , 11-14 Dec. 1988

Pages:416 - 419

[\[Abstract\]](#) [\[PDF Full-Text \(320 KB\)\]](#) **IEEE CNF**

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1 Simultaneous code execution and data storage in a single flash memory chip for real time wireless communication systems

Brown, C.; Hasbun, R.;

Circuits and Systems, 1997. Proceedings of the 40th Midwest Symposium on , Volume: 2 , 3-6 Aug. 1997
Pages:740 - 745 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(672 KB\)\]](#) IEEE CNF

2 A 1.8 V 128 Mb 125 MHz multi-level cell flash memory with flexible read while write

Elmhurst, D.; Bains, R.; Bressie, T.; Bueb, C.; Carrieri, E.; Chauhan, B.; Chrisman, N.; Dayley, M.; De Luna, R.; Fan, K.; Goldman, M.; Govindu, P.; Huq, A.; Khandaker, M.; Kreifels, J.; Krishnamachari, S.; Lavapie, P.; Loe, K.; Ly, T.; Marvin, F.; Melcher, R.; Monasa, S.; Nguyen, Q.; Pathak, B.; Proescholdt, A.; Rahman, T.; Srinivasan, B.; Sundaram, R.; Walimbe, P.; Ward, D.; Zeng, D.R.; Zhang, H.;

Solid-State Circuits Conference, 2003. Digest of Technical Papers. ISSCC. 2003 IEEE International , 2003
Pages:286 - 287 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(378 KB\)\]](#) [\[Extended Object\]](#) IEEE CNF

3 A 1.8 V 64 Mb 100 MHz flexible read while write flash memory [in CMOS]

Pathak, B.; Cabrera, A.; Christensen, G.; Darwish, A.; Goldman, M.; Haque, R.; Jorgensen, J.; Kajley, R.; Ly, T.; Marvin, F.; Monasa, S.; Nguyen, Q.; Pierce, D.; Sendrowski, A.; Sharif, I.; Shimoyoshi, H.; Smidt, A.; Sundaram, R.; Taub, M.; Tran, W.; Trivedi, R.; Walimbe, P.; Yu, E.;

Solid-State Circuits Conference, 2001. Digest of Technical Papers. ISSCC. 2001 IEEE International , 5-7 Feb. 2001
Pages:32 - 33, 424

[\[Abstract\]](#) [\[PDF Full-Text \(220 KB\)\]](#) IEEE CNF

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1 Characteristics of a set of 12.7-mm processor chips

Klein, K.; Koetzle, G.; Miersch, E.F.; Schettler, H.; Schulz, U.; Wagner, O.; Solid-State Circuits, IEEE Journal of , Volume: 22 , Issue: 5 , Oct 1987
Pages:783 - 789

[\[Abstract\]](#) [\[PDF Full-Text \(1464 KB\)\]](#) **IEEE JNL**

2 Co-RAM: combinational logic synthesis applied to software partitions for mapping to a novel memory device

Weber, S.J.; Paul, J.M.; Thomas, D.E.;
Very Large Scale Integration (VLSI) Systems, IEEE Transactions on , Volume:
9 , Issue: 6 , Dec. 2001
Pages:805 - 812

[\[Abstract\]](#) [\[PDF Full-Text \(107 KB\)\]](#) **IEEE JNL**

3 Exploiting symmetry when verifying transistor-level circuits by symbolic trajectory evaluation: a summary

Pandey, M.; Bryant, R.E.;
Circuits and Systems Magazine, IEEE , Volume: 1 , Issue: 4 , 2001
Pages:38 - 42

[\[Abstract\]](#) [\[PDF Full-Text \(171 KB\)\]](#) **IEEE JNL**

4 Reconfigurable caches and their application to media processing

Ranganathan, P.; Adve, S.; Jouppi, N.P.;
Computer Architecture, 2000. Proceedings of the 27th International Symposium
on , 10-14 June 2000
Pages:214 - 224

[\[Abstract\]](#) [\[PDF Full-Text \(1148 KB\)\]](#) **IEEE CNF**

5 Memory design for vectorization

Sharma, N.K.; Iconomidou, A.;

Electronics, Circuits, and Systems, 1996. ICECS '96., Proceedings of the Third IEEE International Conference on , Volume: 2 , 13-16 Oct. 1996
Pages:812 - 815 vol.2

[[Abstract](#)] [[PDF Full-Text \(340 KB\)](#)] [IEEE CNF](#)

6 **A manpack portable LPC 10 vocoder**

Fette, B.; Rimpo, C.; Kish, J.;

Acoustics, Speech, and Signal Processing, IEEE International Conference on ICASSP '84. , Volume: 9 , Mar 1984
Pages:786 - 789

[[Abstract](#)] [[PDF Full-Text \(144 KB\)](#)] [IEEE CNF](#)

7 **Mode partition noise in vertical cavity surface emitting lasers**

Lee, H.L.T.; Ram, R.J.;

Advanced Semiconductor Lasers and Applications/Ultraviolet and Blue Lasers and Their Applications/Ultralong Haul DWDM Transmission and Networking/WDM Components, 2001 Digest of the LEOS Summer Topical Meetings , 30 July-1 Aug. 2001
Pages:2 pp.

[[Abstract](#)] [[PDF Full-Text \(160 KB\)](#)] [IEEE CNF](#)

8 **Soluble dodecylsulfanylphthalocyanines as sensitive coatings for chemical sensors in gas phase**

Altindal, A.; Patel, R.; Zhou, R.; Josse, F.; Ozturk, Z.Z.; Bekaroglu, O.;

Frequency Control Symposium, 1998. Proceedings of the 1998 IEEE International , 27-29 May 1998
Pages:676 - 684

[[Abstract](#)] [[PDF Full-Text \(800 KB\)](#)] [IEEE CNF](#)

9 **A highly available replication control protocol using volatile witnesses**

Paris, J.-F.;

Distributed Computing Systems, 1994., Proceedings of the 14th International Conference on , 21-24 June 1994
Pages:536 - 543

[[Abstract](#)] [[PDF Full-Text \(592 KB\)](#)] [IEEE CNF](#)

10 **Hardware realisation of artificial neural network with application to information coding**

Young, D.; Lee, A.W.H.; Cheng, L.M.;

Neural Networks, 1994. IEEE World Congress on Computational Intelligence., 1994 IEEE International Conference on , Volume: 6 , 27 June-2 July 1994
Pages:3982 - 3985 vol.6

[[Abstract](#)] [[PDF Full-Text \(240 KB\)](#)] [IEEE CNF](#)

11 **Using volatile witnesses to extend the applicability of available copy protocols**

Paris, J.F.;

Management of Replicated Data, 1992., Second Workshop on the , 12-13 Nov. 1992
Pages:30 - 33

12 The GPA machine: a generally partitionable MSIMD architecture
Bridges, T.;
 Frontiers of Massively Parallel Computation, 1990. Proceedings., 3rd Symposium
 on the , 8-10 Oct. 1990
 Pages:196 - 203

[\[Abstract\]](#) [\[PDF Full-Text \(492 KB\)\]](#) **IEEE CNF**

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1 Architecture, memory and interface technology integration of an industrial/ academic configurable system-on-chip (CSoC)

Becker, J.; Vorbach, M.;

VLSI, 2003. Proceedings. IEEE Computer Society Annual Symposium on , 20-21 Feb. 2003

Pages:107 - 112

[\[Abstract\]](#) [\[PDF Full-Text \(633 KB\)\]](#) **IEEE CNF**

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Pages:972 - 977

[\[Abstract\]](#) [\[PDF Full-Text \(104 KB\)\]](#) [IEEE JNL](#)

6 Studies of the angular correlation function of scattering by random rough surfaces with and without a buried object

Zhang, G.; Tsang, L.; Kuga, Y.;

Geoscience and Remote Sensing, IEEE Transactions on , Volume: 35 , Issue: 2 , March 1997

Pages:444 - 453

[\[Abstract\]](#) [\[PDF Full-Text \(392 KB\)\]](#) [IEEE JNL](#)

7 Nonlinear clutter cancellation and detection using a memory-based predictor

Leung, H.;

Aerospace and Electronic Systems, IEEE Transactions on , Volume: 32 , Issue: 4 , Oct. 1996

Pages:1249 - 1256

[\[Abstract\]](#) [\[PDF Full-Text \(804 KB\)\]](#) [IEEE JNL](#)

8 LIGHTNING network and systems architecture

Dowd, P.; Perreault, J.; Chu, J.; Hoffmeister, D.C.; Minnich, R.; Burns, D.; Hady, F.; Chen, Y.-J.; Dagenais, M.; Stone, D.;

Lightwave Technology, Journal of , Volume: 14 , Issue: 6 , June 1996

Pages:1371 - 1387

[\[Abstract\]](#) [\[PDF Full-Text \(1788 KB\)\]](#) [IEEE JNL](#)

9 Some construction methods for error-correcting (d,k) codes

Nasiri-Kenari, M.; Rushforth, C.K.;

Communications, IEEE Transactions on , Volume: 42 , Issue: 234 , February-April 1994

Pages:958 - 965

[\[Abstract\]](#) [\[PDF Full-Text \(760 KB\)\]](#) [IEEE JNL](#)

10 Single event upsets for Space Shuttle flights of new general purpose computer memory devices

O'Neill, P.M.; Badhwar, G.D.;

Nuclear Science, IEEE Transactions on , Volume: 41 , Issue: 5 , Oct. 1994

Pages:1755 - 1764

[\[Abstract\]](#) [\[PDF Full-Text \(804 KB\)\]](#) [IEEE JNL](#)

11 Asymptotically optimum finite-memory detectors in ϕ -mixing dependent processes

Cheung, J.; Kurz, L.;

Signal Processing, IEEE Transactions on [see also Acoustics, Speech, and Signal Processing, IEEE Transactions on] , Volume: 42 , Issue: 9 , Sept. 1994

Pages:2344 - 2354

[\[Abstract\]](#) [\[PDF Full-Text \(696 KB\)\]](#) [IEEE JNL](#)

12 An iteration partition approach for cache or local memory thrashing on parallel processing

Fang, J.Z.; Lu, M.;

Computers, IEEE Transactions on , Volume: 42 , Issue: 5 , May 1993

Pages:529 - 546

[\[Abstract\]](#) [\[PDF Full-Text \(1464 KB\)\]](#) IEEE JNL

13 Knowledge-based system for boundary detection of four-dimensional cardiac magnetic resonance image sequences

Suh, D.Y.; Eisner, R.L.; Mersereau, R.M.; Pettigrew, R.I.;

Medical Imaging, IEEE Transactions on , Volume: 12 , Issue: 1 , March 1993

Pages:65 - 72

[\[Abstract\]](#) [\[PDF Full-Text \(876 KB\)\]](#) IEEE JNL

14 Bit-level soft-decision decoding of Reed-Solomon codes

Vardy, A.; Be'ery, Y.;

Communications, IEEE Transactions on , Volume: 39 , Issue: 3 , March 1991

Pages:440 - 444

[\[Abstract\]](#) [\[PDF Full-Text \(456 KB\)\]](#) IEEE JNL

15 The polarization angular correlation function for detection of object buried in forest above rough surface

Wang Xiande; Zhang Zhongzhi; Fu Junmei;

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IEEE 2000 International , Volume: 1 , 24-28 July 2000

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